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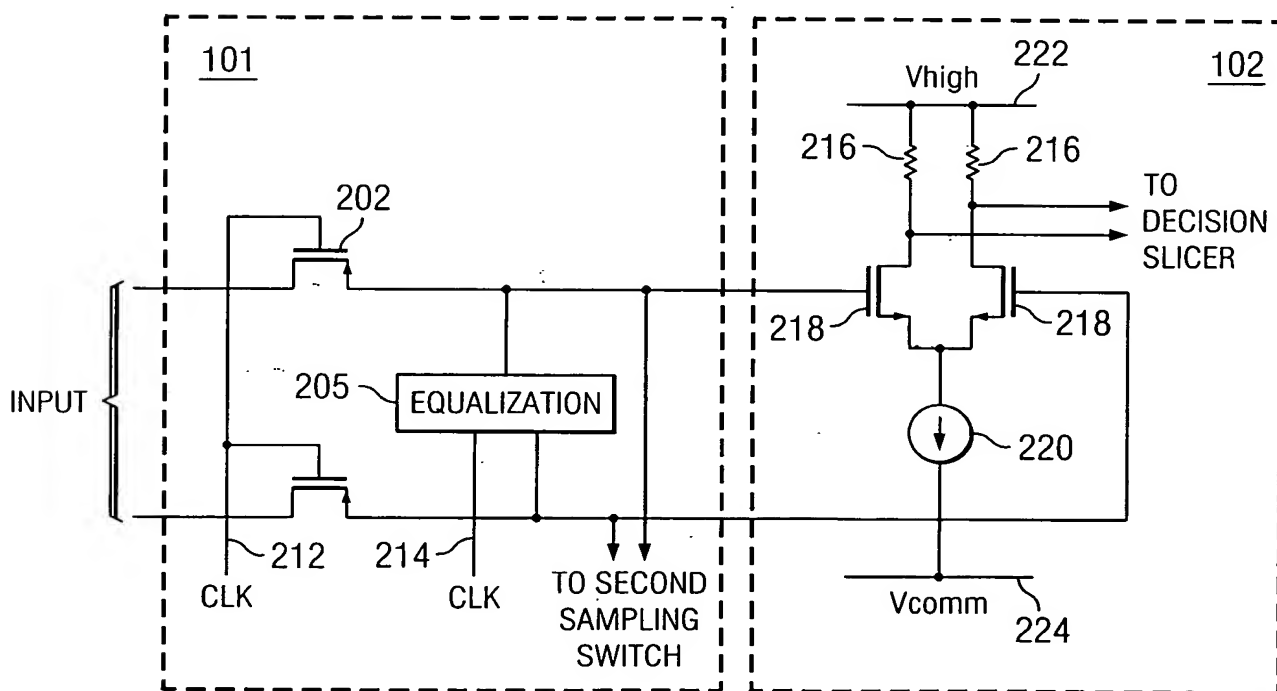
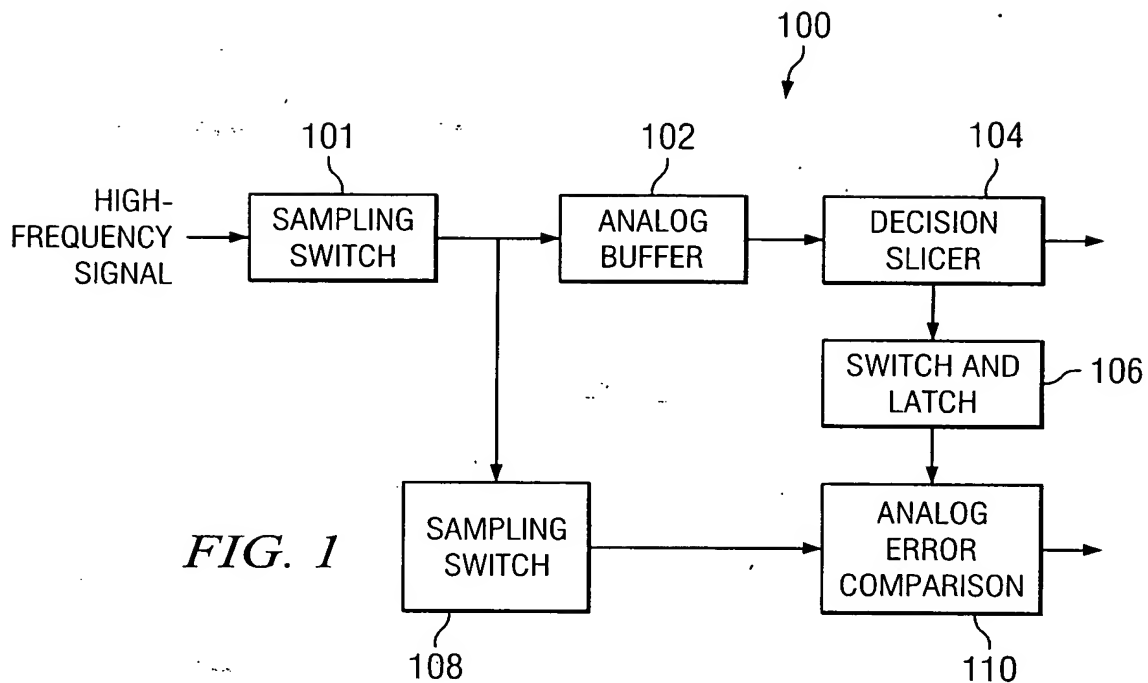


FIG. 3 is a schematic diagram of a circuit 104. The circuit includes a clock input CLK (302) and an input 'in'. The circuit is composed of several transistors (304, 306) and a feedback loop (306). The output of the circuit is labeled 104.

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FIG. 5

